

#### IN THE UNDER TES PATENT AND TRADEMARK OFFICE

Applicant: Hongyong Zhang

Art Unit: 2812

Serial No.: 09/362,808

Examiner:

Filed

: July 28, 1999

Title

: METHOD OF FABRICATING SEMICONDUCTOR DEVICE

Assistant Commissioner for Patents

Washington, D.C. 20231

MAR 1 7 2000

## TRANSMITTAL LETTER

TECHNOLOGY CENTER 2800

Correspondence relating to this application is enclosed. The required fees are computed below. Please apply any charges not covered, or any credits, to Deposit Account No. 06-1050.

Total Claims

30

10 3.

\$180

Independent

6

3

20

\$234

TOTAL FEE DUE

A check for \$414 is attached.

\$414

Respect fully submitted,

Sco##

C. Harris

Req. No. 32,030

SCH/smr

PTO Customer No. 20985 Fish & Richardson P.C.

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San Diego, CA 92122

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### CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231.

Date of Deposit

Signature

Typed or Printed Name of Person Signing Certificate



Attorney's Doc No.: 07977/088002/US3155

# IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant

: H. Zhang

Art Unit: 2812

Serial No. : 09/362,808

Examiner : Unknown

Filed

: July 28, 1999

Title

: METHOD OF FABRICATING SEMICONDUCTOR DEVICE

Assistant Commissioner for Patents

Washington, D.C. 20231

## SUPPLEMENTAL PRELIMINARY AMENDMENT

TECHNOLOGY CENTER 2800

Sir:

Prior to initial examination and supplemental to the Preliminary Amendment filed January 18, 2000, kindly amend the above-identified application as follows.

## In the Claims:

Please amend the following claim.

6. (Amended) A semicondyctor device comprising:

a semiconductor layer formed over a substrate having an insulating surface, said semiconductor layer having at least channel, source and drain regions;

a gate insulating film over [a] said semiconductor layer;

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01 FC:103 02 FC:102

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Signature

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